IN THE SPECIFICATION

Please amend the specification as follows:

Please replace the paragraph beginning at page 6, line 21, with the following rewritten paragraph:

--   {Description of the Overall Structure of the Information Processing System}

The information processing system shown in Fig. 1 comprises a processing device 10 that performs information operations, a channel 11 that performs input/output controls such as data transfer, an external storage device 13, and a control device 12 that controls data sending and receiving between the processing device 10 and the external storage device 13. The control device 12 has recording and regeneration functions for a control information memory 122, which stores control information that is used when controls are implemented. In the information processing system, based on an instruction from the processing device 10, a data input/output instruction is issued to logical devices 132, which are defined inside the external storage device 13, via the channel 11 and a cache memory 124 inside the control device 12. The data that are the subject of input/output request from the processing device 10 are normally stored in the cache memory 124 on a logical track-by-a logical track 20 basis, as shown in Fig. 2. --

Please replace the paragraph beginning on page 12, line 3, with the following rewritten paragraph as follows:

--   {Processing to Recognize the Impact Look-Ahead Processing Performed at Present Would Have on Other Input/Output Requests}

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Fig. 5 shows a flowchart of a processing to recognize the impact a look-ahead processing performed at present would have on other input/output requests. --

Please replace the paragraph beginning on page 13, line 6, with the following rewritten paragraph as follows:

-- [Processing to Load One or More Blocks in Specific Data Space to Cache Memory]

Fig. 6 shows as a flowchart a processing to load one or more blocks (logical tracks) in a specific data space (database) to a cache memory. --

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